King Saud University

Electrical Engineering Department

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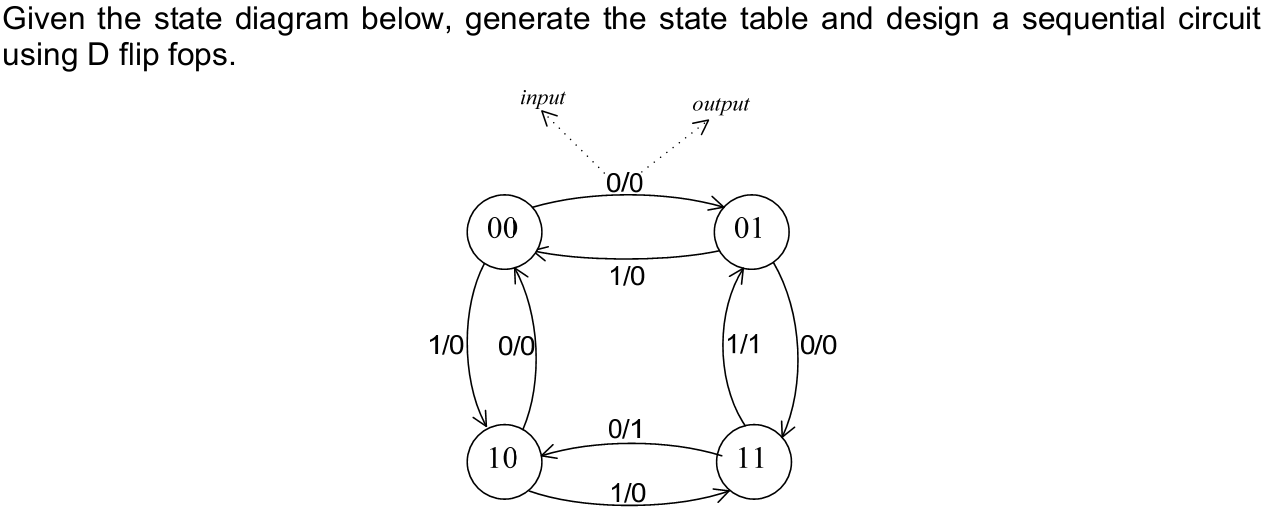
EE\_208: HW#4

Name:

Number:

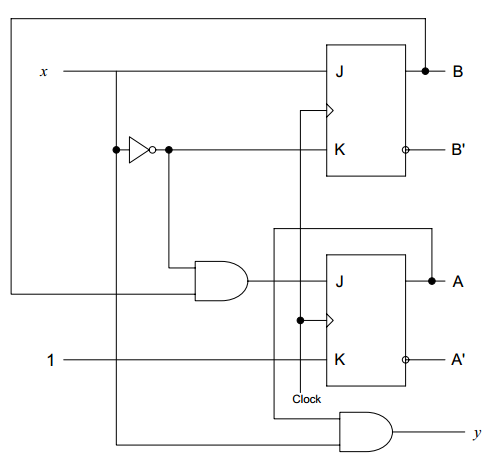
Deadline: 7-May

Q.1 ( 4 Marks )

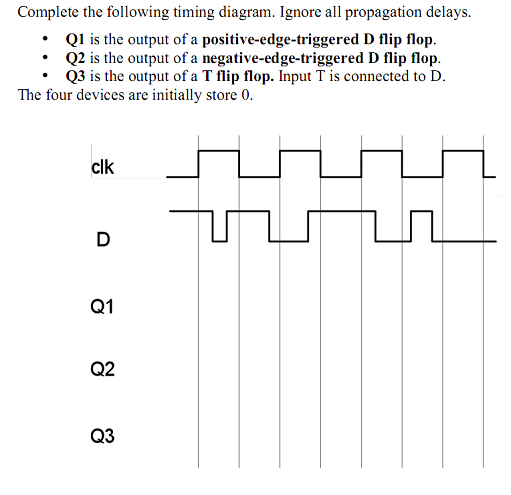


Q.2 (4 marks) For the following sequential circuit:

1. Drive the next state equation
2. Complete the state Table
3. Draw the state diagram



Q.3 ( 2 marks )



Hint: in the last case (Q3), T is the clock and since T is connected to D, let D be the clock.(trigger positive edge)