



**King Saud University**  
**College of Computer and Information Sciences**  
**Department of Computer Engineering**

**CEN 214 - Logic Design I 3(3-0-1), Section 20349 & 37005**  
**Semester II, Academic Year 2014-2015**

**Required Course**

**Time: Section 20349 MW 08:00-09:50**  
**Section 37005 MW 13:00-14:50**

**Course Description (catalog):**

This course provides students with basic knowledge on synchronous sequential machines. Topics include: Memory elements, Sequential circuits analysis, Sequential circuits design, Registers and Counters, Memory and Programmable Logic Devices, Register Transfers and Datapaths, Sequencing and Control.

**Prerequisites:** - Courses CEN 200

- Topics

- Combinational Logic Design

**Textbook(s) and/or Other Required Materials:**

**Primary:** M. Morris Mano and Charles R. Kime, *Logic and Computer Design Fundamentals*, 4<sup>th</sup> Ed, 2007, Prentice Hall

**Supplementary:**

Morris Mano, *Digital Design*, 3rd Ed, 2000, Prentice Hall.

**Course Learning Outcomes:** This course requires the student to demonstrate the following:

1. Design and describe the operation of basic memory elements.
2. Analyse the behaviour of synchronous and asynchronous machines
3. Design synchronous and asynchronous sequential machines
4. Describe and implement finite state machines (FSM)
5. Apply the concepts of basic timing issues, including clocking, timing constraints, and propagation delays during the design process.
6. Use Memory and Programmable Logic Devices.
7. Use basic combinational and sequential components in typical datapath designs.

**Major Topics covered and schedule in weeks:**

Memory elements	2
Sequential circuits analysis and Design	2
Registers and Counters	2
Memory and Programmable Logic Devices	2
Register Transfers and Datapaths	2
Sequencing and Control	2
Asynchronous Circuit Analysis and Design	2
Review and evaluation	2

**Contribution of Course to Meeting Curriculum Disciplines:**

Curriculum Discipline	Percentage
Mathematics and Basic Science	10
Engineering Science	
Engineering Design	90
General Education	

**Relationship of course to program objectives:**

1. Provide robust understanding of the fundamental areas of computer engineering.
2. Succeed in lifelong learning programs to remain current professionals contributing to the advancement of the global industry.
3. Build strong ethical and behavior system that will assist graduates to face real-life professional and general challenges.

**Relationship of Course to Program Outcomes**

Outcome	Program Outcome Description	Level of Contribution
(a)	an ability to apply knowledge of mathematics, science, and engineering	H
(b)	an ability to design and conduct experiments, as well as to analyze and interpret data	
(c)	an ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability	M
(d)	an ability to function on multidisciplinary teams	
(e)	an ability to identify, formulate, and solve engineering problems	H
(f)	an understanding of professional and ethical responsibility	
(g)	an ability to communicate effectively	
(h)	the broad education necessary to understand the impact of engineering solutions in a global, economic, environmental, and societal context	
(i)	a recognition of the need for, and an ability to engage in life-long learning	
(j)	a knowledge of contemporary issues	
(k)	an ability to use the techniques, skills, and modern engineering tools necessary for engineering practice.	M

**H=High, M= Medium, L=Low**

## Course Policies:

No late homework will be accepted.

The quizzes may be pop or announced, and may be given at anytime during class-time □

Homework assignments are considered individual efforts. However, students are encouraged to share thoughts with others. **ABSOLUTELY NO COPYING.** Academic dishonesty cases will be dealt with severely. □ All exams are closed book.

## Assessment Plan for the Course

Student's performance in homework, quizzes, and exams.

Attendance	5%
Assignments	15%
Two Midterm exams	40%
Final exam	40%

**THE FINAL EXAM WILL BE COMPREHENSIVE.**

## Current Instructor, Department, Office Hours and Date:

Dr. Haikel Salem Hichri

Department of Computer Engineering

Office #: G080 (ALISR lab, ground floor, CCIS)

Phone #: 0114696294

Office Hours: Mon 12:00-13:00, 15:00-16:00, Tue 8:00-11:00, 12:00-13:00, Wed 10:00-11:00, Thu 08:00-11:00

Email: [hhichri@KSU.EDU.SA](mailto:hhichri@KSU.EDU.SA)

<http://fac.ksu.edu.sa/hhichri/>