

Kais Ben Jilani BELWAFI,



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M.Sc. Real-Time Computer Science engineering

Profile:

Kais Ben Jilani Belwafi is an Embedded System Design Engineer. I have my master degree from ENISO with very well mark in the Smart Communication Systems. I have also Engineer degree in the real time computer science engineer. I have participated in different trainings related to the microcontroller-based architecture and Hardware/software system design. I have been involved in a number of industrial and research projects in the area of design of real-time embedded system design. I have also a good experience in developing both Hardware and Software for Real-time Embedded systems. I have good skills with much computer software.

Education:

Since September 2013

PhD student in intelligent system in the ENSEA in France and ENISO in Tunisia.

2011-2012:

Master Degree in Smart Communication Systems from the College of Engineering in Sousse University (ENISO National School of Engineering of Sousse), Tunisia.

2008-2010:

Engineer Degree in Applied Sciences and Technologies for Embedded Software from the Higher Institute of Applied Science and Technology of Sousse (ISSATS), Tunisia.

2004-2007:

B.Sc. Degree in Electronics, (DUT in Electronic and Microcontrollers). Higher Institute of Applied Science and Technology of Sousse (ISSATS), Tunisia.

Technical Skills:

- Operating Systems: Windows, Linux
- Programming Languages: C/C++, Assembly, VHDL, Verilog, Matlab, MPLAB, Java, J2ME, Pascal, TCL/TK, Python, Shell, SQL, UML, HTML etc...
- Embedded Systems: (RTEMS, Micruin), Microcontrollers: (Assembler, MicroC).
- Data Base (Access, Oracle, SQL), Software (Windev, Vb net, Netbeans), etc...
- FPGA Tools: Xilinx, Mentor Graphics, QuartusII, etc...
- Electronics: Proteus, Eagle, Workbench, PCB Designer ...etc.

Professional experience:

September 2012-June 2013:

Teacher in High School of engineering of Sousse

Teaching matters Security and Architecture of embedded and advanced system.

Since April 2012:

Engineer: STIM, Sfax, Tunisia

Project: development of kernel Linux (As Ubuntu, UC/Os) and a software to encrypt screen captured from camera using an advanced FPGA board. Responsible for many industrial tasks, repair and improve the functional machine.

July 2011-January 2012:

Research Assistant in the Prince Sultan Advanced Technologies Research Institute (PSATRI), King Saud University (KSU).

Project: Study and development of an embedded system dedicated to radar detection.

April 2010-June 2010:

Engineer in Prince Sultan Advanced Technologies Research Institute (PSATRI) of the King Saud University (KSU).

Project: Contribution in the development of an embedded system for target detection.

August 2010-March 2011:

Engineer in the Internship: Celeria-Bizerte, Tunisia

Project: Development of an embedded system dedicated to electric car (as an Electronic project manager).

July 2009:

Internship: DIRALEC, Sfax, Tunisia.

Project: Siemens Automats Programming.

June-August 2008:

Engineer: Electronics Laboratory in the Study and Electric Realization Company, Sfax, Tunisia.

Project: Implementation of Electronic Cards (battery charger, transformer protection card, battery charger design based on PIC16F877).

February-June 2007:

Internship: Development box; STIM Incubator Start-up, Sfax, Tunisia

Project: Study and development of a control board for a plastic grinder.

Training:

June-July 2006: Training in the Electronic Centre; National Telecommunication Company, Sousse, Tunisia; to become familiar with the centre services.

July-August 2005: Training period in the Tunisian Company of Electricity and Gas (STEG), Objective: Control of digital system and electronic equipment maintenance.

7-Days: Training period in the PSATRI from National Instrument.

Objective: Learn Labview system.

Language Skills:

Arabic: Native

French: Excellent writing and oral skills

English: Good written and oral skills (Certificate in English Language in 2007, Sousse).

References:

Journals

- Ridha Djemal, Kais Belwafi, Walid Kaanich, Saleh A. Alshebeili "A novel hardware/software embedded system based on automatic censored target detection for radar systems". AEU Elsevier.

- Ridha Djemal, Kais Belwafi, Saleh A. Alshebeili "**Adaptive CFAR embedded system architecture for target detection**". DAEM springer.

Conference

- Ridha Djemal, Kais Belwafi, Saleh A. Alshebeili "**An FPGA-Based Implementation of HW/SW architecture for CFAR Radar Target Detector**". ICM-IEEE.