

LabWork#7

Q#1

Design a 8-to-1-line multiplexer with two 4-to-1-line multiplexers and one 2-to-1-line multiplexer.

Q#2

- a) Construct the 2-to-4-line decoder
- b) Construct a 3-to-8-line decoder with two 2-to-4-line decoders without enable.

Q#3

Implement full adder by 3 to 8 Decoder