

IE 337: Automatic control systems 36			(3,1,1)	
Catalog Data	Process control fundamentals using programmable logic controllers			
Prerequisite	Math 204, GE211			
Co requisite				
Level				
Textbook	<ul> <li>* Modern Control Systems, R.C. Dorf, Addison Wesley.</li> <li>* Logical Design of Automation System, Sunder B. Friedman, Prentice Hall.</li> </ul>			
Reference	*Automatic Control Systems, B.C. Kuo, Prentice-Hall, Inc.			
	*PC	*PC-Based Instrumentation and Control, Mike Tooley, Newnes, An Imprint of		
		Butterworth-Heinemann Ltd. (1991).		
	*Ma	*Manufacturing Automation Using PLC, Ali M Alsamhan, Saied M Darwish, Grant		
	16/424 of Research center College of Engineering, King Saud			
	University			
	*David W. Pessen, Industrial Automation, Circuit Design & Components"			
	, A Wiley-Interscience Publication, John Wiley & Sons, 1989.			
Course	Design an automated system to meet defined industrial operations using PLC [c]			
Learning	Practicing and understanding Relay Ladder logic to programing PLC using experiments [b]			
Outcomes				
(CLU) Tonics	1	Introduction to factory automation	1 classes wook 1	
(classes)	1. 2	Programmable logic controller and relay ladder logic	4 classes- week 1	
(classes)	2.	Numbering systems	1 classes-week 2	
	<u>э</u> . л	Fundamentals of computer logics	5 classes- week 3	
	- <del>1</del> . 5	Logic sensors and actuators	5 classes- week 4-5	
	6	Pneumatic sensors actuators and control methods	5 classes- week 6	
	7	Relay ladder logic design of common machine sequences	10 classes- week 7-8	
	8	PLC timers counters registers and analog input/outputs	10 classes- week 9-10	
	9	Illustrated industrial automation applications	5 classes- week 10	
	<u> </u>	Total Classes (10 weeks)	50 classes	
Lab. work	1	Logic gate networks using programmable logic controller	3 Lab	
	2	Electrical proximity sensors and actuators.	1 Lab	
	3	Pneumatic actuators, sensors and logic network	1 Lab	
	4	Relay ladder logic of common control sequence	3 Lab	
	5	Timer, counter, analog input /outputs	3 Lab	
Equipment	PLC programming using relay ladder logic.			
Estimated	Engineering Science: 2 credit hours (60%)			
Category	Engineering Design: 1 credit hour (40%)			
Content	20 M	20 Marks 1 <sup>st</sup> midterm examination + 20 Marks 2 <sup>nd</sup> Midterm examination, 20 Marks Lab		
	work. Total 60 Marks semester work, 40 Marks Final Exam.			
Prepared by	Prof	Prof Ali M Alsamhan, Dr Bashir Salah		
Exam. Date	Mid-1 (end week 6) Mid-2 (end week 9) – [ 8/10/2022 – 29/10/2022 1 <sup>st</sup> semester 2022]			